

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: OZASA, et al.
Serial No.: 10/730,839
Filed: December 8, 2004
Confirmation No.: Unknown

Examiner: Unknown
Group Art Unit: Unknown
Docket: 10873.1358US01
Notice of Allow. n/a
Date:

Due Date: March 8, 2004

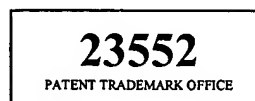
Title: CURRENT SOURCE CIRCUIT AND AMPLIFIER USING THE SAME

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: , Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 24, 2004.

By: Peggy Kerkhove
Name: Peggy Kerkhove

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Sir:

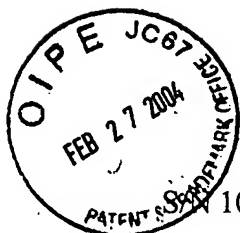
We are transmitting herewith the attached:

- ☒ Transmittal Sheet in duplicate containing Certificate of Mailing
- ☒ Information Disclosure Statement, Form 1449, 3 Reference(s)
- ☒ Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers or any future reply, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

MERCHANT & GOULD P.C.
P.O. Box 2903, Minneapolis, MN 55402-0903
612.332.5300

By: Douglas P. Mueller
Name: Douglas P. Mueller
Reg. No.: 30,300
DPM/pjk



10/730,839

PATENT

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Applicant:	OZASA, et al.	Examiner:	Unknown
Serial No.:	10/730,839	Group Art Unit:	Unknown
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By:

Name: Peggy Kerkhove

INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(b))

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted within three months of the filing date of the above-identified application, which is not an application under 37 C.F.R. § 1.53(d). Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

In accordance with 37 C.F.R. §1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

A concise explanation of the relevance of each non-English language document or other information is as follows (37 C.F.R. §1.98(a)(3)):

An English abstract has been provided for JP 2-124609.

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicants reserve the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicants do not represent that a

reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

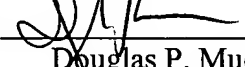
Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

MERCHANT & GOULD P.C.
P.O. Box 2903
Minneapolis, MN 55402-0903
(612)332.5300

Dated: February 24, 2004

By: 
Douglas P. Mueller
Reg. No. 30,300

DPM/pjk



FORM 1449*

INFORMATION DISCLOSURE STATEMENT

IN AN APPLICATION

(Use several sheets if necessary)

Docket Number:

10873.1358US01

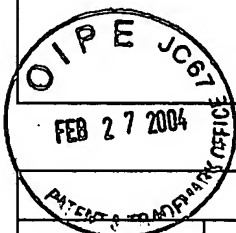
Application Number:

10/730,839

Applicant: OZASA, et al.

Filing Date: December 8,
2003

Group Art Unit: Unknown



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	2-124609	May, 1990	Japan			abstract	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"Semiconductor Circuit Design Technology", Nikkei Business Publishers Inc., edited by T. Tamai, 1st edition, p. 302
		"CMOS Analog Circuit Design Second Edition", p. 196, published by OXFORD, Phillip E. Allen and Douglas R. Holberg

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PATENT TRADEMARK OFFICE

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.